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PATENT

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October 25, 2006

Date

Jennifer A. Steele

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Brent Keeth, Brian M. Shirley,
Charles H. Dennison and
Kevin J. Ryan

Attorney Docket No.: 500644.01

Patent No. : US 6,862,654 B1

Issued : March 1, 2005

Title : METHOD AND SYSTEM FOR USING DYNAMIC RANDOM ACCESS MEMORY AS
CACHE MEMORY

NOTIFICATION OF ERRORS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following errors were noted in a review of the above-identified letters patent. One of these errors was inadvertently made in the original application, while the others occurred in the printing of the patent. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicants request that this notification be placed in the Patent and Trademark Office file.

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Column 2, Lines 34-35	"complimentary"	--complementary--
Column 4, Lines 1-2 and 4	"complimentary"	--complementary--

Column 4, Line 56	"Therefore, a"	--Therefore, an--
Column 4, Line 61	"for to wait for"	--to wait for--
Column 4, Line 62	"the cache system"	--of the cache system--
Column 5, Line 54	"complimentary"	--complementary--
Column 6, Line 14	"wherein the an"	--therein the--
Column 6, Line 15	"complimentary"	--complementary--
Column 7, Lines 2 and 44	"complimentary"	--complementary--
Column 9, Line 4	"is directed and"	--is directed; and

Respectfully submitted,

Date: October 25, 2006

By: 

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Enclosure:
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